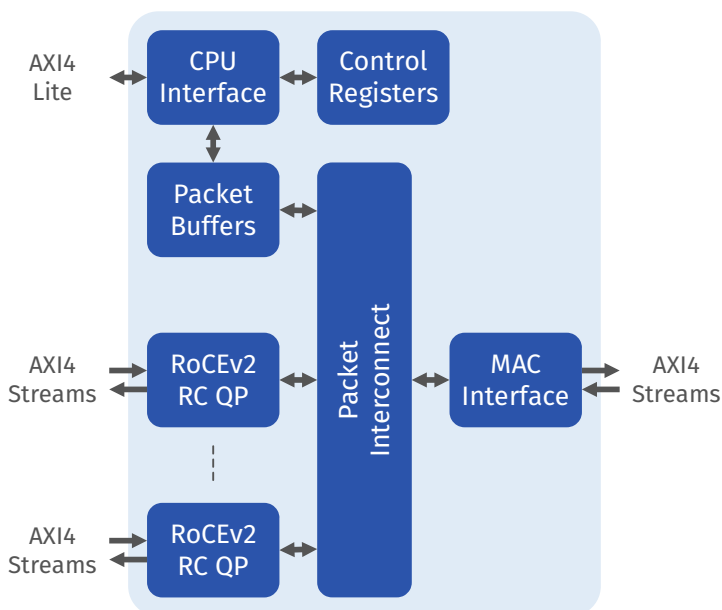
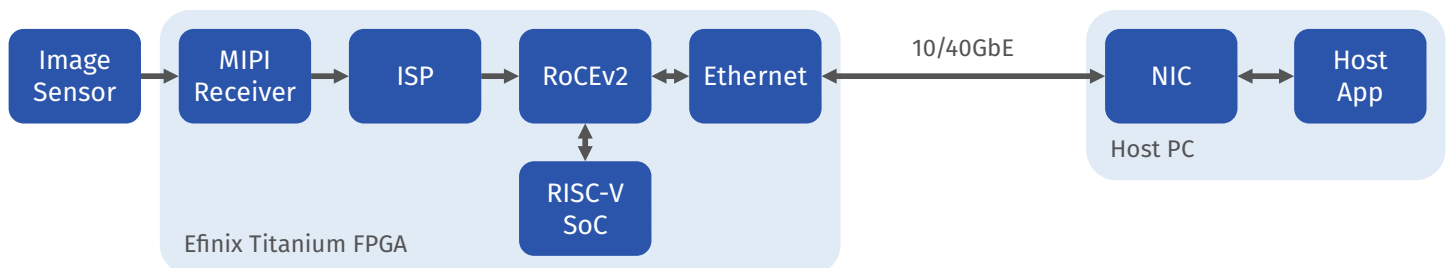




# RoCEv2 Video Streaming Demo for Efinix Titanium FPGAs

## Overview

- Implementation of the hardware-offloaded RDMA data transport
  - High performance and efficiency
  - Reliability and data consistency
  - Low latency
- Built around the imavix engineering RoCEv2 FPGA IP core
- Implemented in the Efinix Titanium power-efficient FPGA
- Simple ibverbs based host application for data visualization
  - Available in full sources as a reference



## RoCEv2 FPGA IP Core

- RoCEv2 reliable connection send/receive queue pairs
- Bidirectional, message-based, remote DMA data transfer
- Hardware implementation of the transport protocol
- Fully customizable core configuration
- Supported data rates up to 100 Gb/s
- Delivered with basic embedded CPU control software

## About imavix engineering

- FPGA IP cores with related development and consultancy services
- Image sensor and camera interfacing
- PCIe host interfaces with DMA streaming engines
- GenICam based standard machine vision interfaces
- Ethernet and GigE Vision transport layers including the RDMA/RoCEv2 technology